

HEWLETT-PACKARD COMPANY
Intellectual Property Administration
P. O. Box 272400
Fort Collins, Colorado 80527-2400

PATENT APPLICATION

ATTORNEY DOCKET NO. 10008019 - /

IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): VENKITAKRISHNAN ET AL.

Confirmation No.: 7710

Application No.: 09/942,328

Examiner: Kim, Hong Chong

Filing Date: 8/28/2001

Group Art Unit: 2186

Title: A STREAMLINED CACHE COHERENCY PROTOCOL SYSTEM AND METHOD FOR A
MULTIPLE PROCESSOR SINGLE CHIP DEVICE

RECEIVED
CENTRAL FAX CENTER

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

APR 22 2004

TRANSMITTAL LETTER FOR RESPONSE/AMENDMENT

Sir:

Transmitted herewith is/are the following in the above-identified application:

(X) Response/Amendment () Petition to extend time to respond
 () New fee as calculated below () Supplemental Declaration
 (X) No additional fee (Address envelope to "Mail Stop Non-Fee Amendment")
 () Other: _____ (fee \$ _____)

| CLAIMS AS AMENDED BY OTHER THAN A SMALL ENTITY | | | | | | |
|--|--------------------------------------|-----------------------|--|------------------------|----------|---------------------|
| (1) FOR | (2) CLAIMS REMAINING AFTER AMENDMENT | (3) NUMBER EXTRA | (4) HIGHEST NUMBER PREVIOUSLY PAID FOR | (5) PRESENT EXTRA | (6) RATE | (7) ADDITIONAL FEES |
| TOTAL CLAIMS | 21 | MINUS | 20 | = 1 | X \$18 | \$ 18 |
| INDEP. CLAIMS | 3 | MINUS | 3 | = 0 | X \$86 | \$ 0 |
| [] FIRST PRESENTATION OF A MULTIPLE DEPENDENT CLAIM | | | | | + \$290 | \$ 0 |
| EXTENSION FEE | 1ST MONTH \$110.00 | 2ND MONTH \$420.00 | 3RD MONTH \$950.00 | 4TH MONTH \$1480.00 | | \$ 0 |
| OTHER FEES | | | | | | \$ |
| TOTAL ADDITIONAL FEE FOR THIS AMENDMENT | | | | | | \$ 18 |

Charge \$ 18 to Deposit Account 08-2025. At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account 08-2025 pursuant to 37 CFR 1.25. Additionally please charge any fees to Deposit Account 08-2025 under 37 CFR 1.16, 1.17, 1.19, 1.20 and 1.21. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

VENKITAKRISHNAN ET AL.

By

Wendell J. Jones

Attorney/Agent for Applicant(s)
Reg. No. 45,881

Date: 4/22/2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICERECEIVED
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In Re Application of:

Date: April 22, 2004

APR 22 2004

Venkitakrishnan et al.

Serial No.:09/942,328

Group Art Unit: 2186

Filed: August 28, 2001

Examiner: Kim, Hong Chong

For: A STREAMLINED CACHE COHERENCY PROTOCOL SYSTEM
AND METHOD FOR A MULTIPLE PROCESSOR SINGLE CHIP
DEVICE

OFFICIAL

Honorable Commissioner of Patents and Trademarks
Alexandria, VA 22313-1450RESPONSE TO OFFICE ACTION

Sir:

In response to the Office Action dated January 22, 2004, please enter the following amendments and remarks into the above-identified application:

Amendments begin on page 2 of this paper.

Remarks/Arguments begin on page 6 of this paper.